



इलेक्ट्रॉनिक्स एवं  
सूचना प्रौद्योगिकी मंत्रालय  
MINISTRY OF  
ELECTRONICS AND  
INFORMATION TECHNOLOGY



विज्ञान एवं प्रौद्योगिकी विभाग  
DEPARTMENT OF  
SCIENCE & TECHNOLOGY



ORGANISED BY



# Powering the Future

HPC • AI • QUANTUM



**SUPER  
COMPUTING  
INDIA 2025**

**DECEMBER 09 – 13, 2025**

Venue: Manipal Institute of Technology (MIT), Bengaluru - KA - India

**ORGANISED BY**

Centre for Development of Advanced Computing (C-DAC), Bengaluru

<https://sci25.supercomputingindia.org>





At the forefront of India's technological revolution stands premier R&D organisation **Centre for Development of Advanced Computing (C-DAC)**, established in 1988 under the aegis of the Ministry of Electronics and Information Technology (MeitY). C-DAC pioneered the PARAM series of supercomputers placing India firmly on the global map as a leader in High Performance Computing (HPC).


Today, India is undergoing a remarkable digital and technological transformation, driven by flagship national initiatives including **National Supercomputing Mission**, India AI, India Semiconductor Mission, and National Quantum Mission, spearheaded by MeitY and Department of Science and Technology (DST), the twin pillars of India's science and technology governance.

The National Supercomputing Mission (NSM) has established a robust, distributed network of supercomputing infrastructure across 28 premier institutions nationwide laying the foundation for cutting-edge research, innovation, and capacity building in HPC.

Together, the strategic initiatives embody a bold national vision to position India as a global technology powerhouse. C-DAC remains at the core of this effort, driving advancements in HPC, Artificial Intelligence, and Quantum Computing, while fostering technological self-reliance and building collaborative platforms that bridge academia, industry, and government.

The Supercomputing Conference is a global event for High Performance Computing, networking, storage, and data analysis. The conference unites researchers, industry experts, and government leaders to showcase groundbreaking innovations and share cutting-edge research. Serving as a vital hub for collaboration and knowledge exchange, the event drives the advancement of computational science and engineering in the areas of HPC, AI, Quantum.

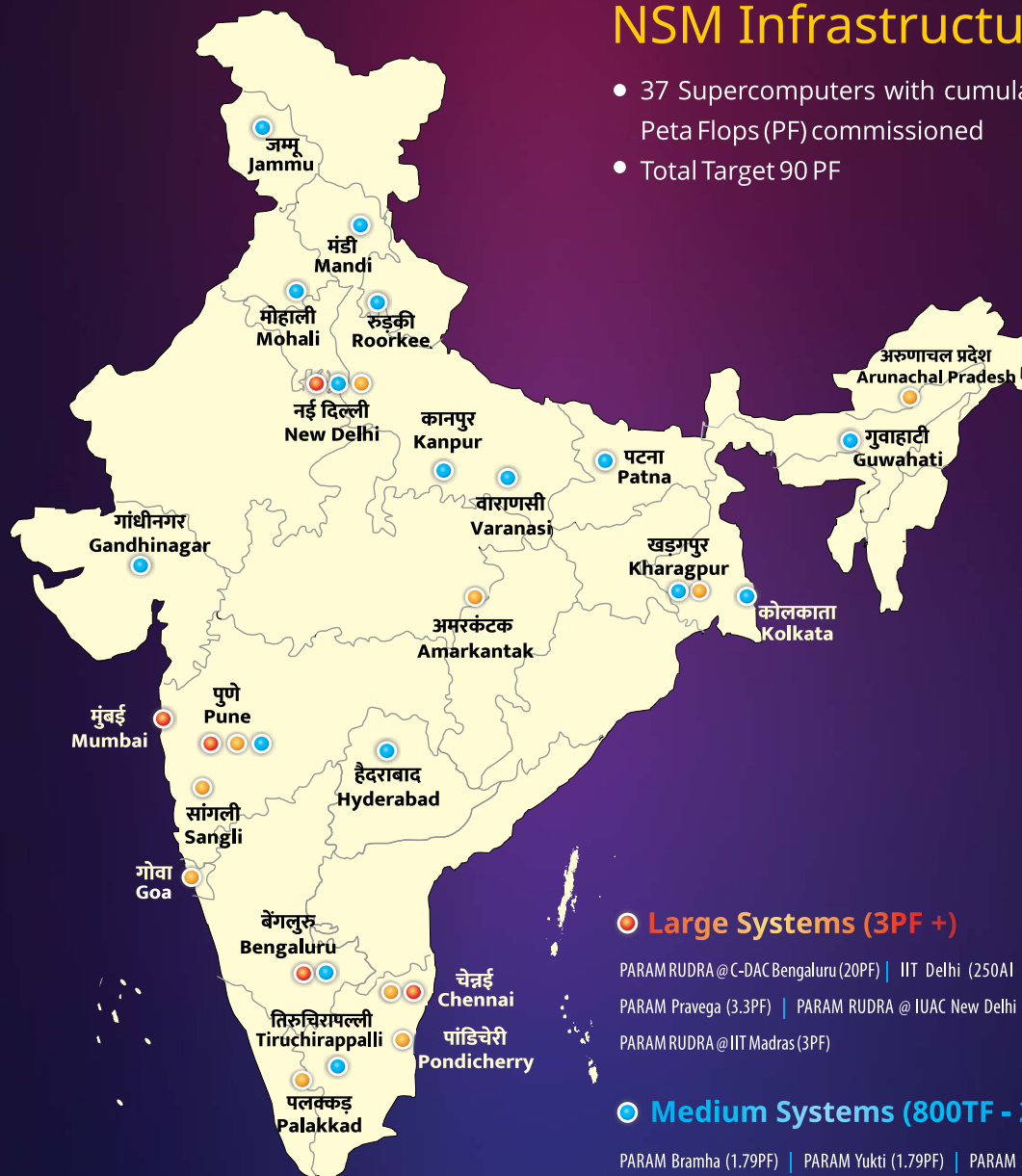
Supercomputing India (SCI2025) is the inaugural edition of Supercomputing Conference series in India which will serve as the platform to showcase and explore the latest breakthroughs in Supercomputing, AI, and Quantum technologies, enabling global collaboration at the intersection of science, technology, and innovation.



# National Supercomputing Mission

## NSM Infrastructure

- 37 Supercomputers with cumulative capacity of 39 Peta Flops (PF) commissioned
- Total Target 90 PF



### Large Systems (3PF +)

PARAM RUDRA @ C-DAC Bengaluru (20PF) | IIT Delhi (250AI PF / 6PF) | PARAM Siddhi AI (5.26PF)  
PARAM Pravega (3.3PF) | PARAM RUDRA @ IUAC New Delhi (3PF) | PARAM RUDRA @ IIT Bombay (3PF)  
PARAM RUDRA @ IIT Madras (3PF)

### Medium Systems (800TF - 2PF)

PARAM Bramha (1.79PF) | PARAM Yukti (1.79PF) | PARAM Shakti (1.66PF) | PARAM Sanganak (1.66PF)  
PARAM Ganga (1.66PF) | NIC Delhi (50AI PF / 1.3PF) | PARAM RUDRA @ GMRT/NCRA Pune (1PF)  
PARAM Seva (838TF) | PARAM Smriti (838TF) | PARAM Utkarsh (838TF) | PARAM Porul (838TF)  
PARAM Himalaya (838TF) | PARAM Shivay (838TF) | PARAM Kamrupa (838TF)  
PARAM RUDRA @ IIT Patna (838TF) | PARAM Ananta (838TF) | PARAM RUDRA @ SNBNCBS Kolkata (838TF)  
PARAM RUDRA @ IIT Jammu (838TF)

### Small Systems (less than 500TF)

PARAM Spoorthi (100TF) | PARAM VIDYA @ Walchand CoE Sangli (52TF) | PARAM VIDYA @ DTU New Delhi (52TF)  
PARAM VIDYA @ IGNTU Amarkantak (52TF) | PARAM VIDYA @ NIT Arunachal Pradesh (52TF)  
PARAM VIDYA @ IIT Goa (52TF) | PARAM VIDYA @ Pondicherry University (52TF)  
PARAM VIDYA @ IIT Madras (52TF) | PARAM VIDYA @ C-DAC Pune (52TF) | PARAM VIDYA @ IIT Palakkad (52TF)  
PARAM VIDYA @ IIT Kharagpur (52TF)

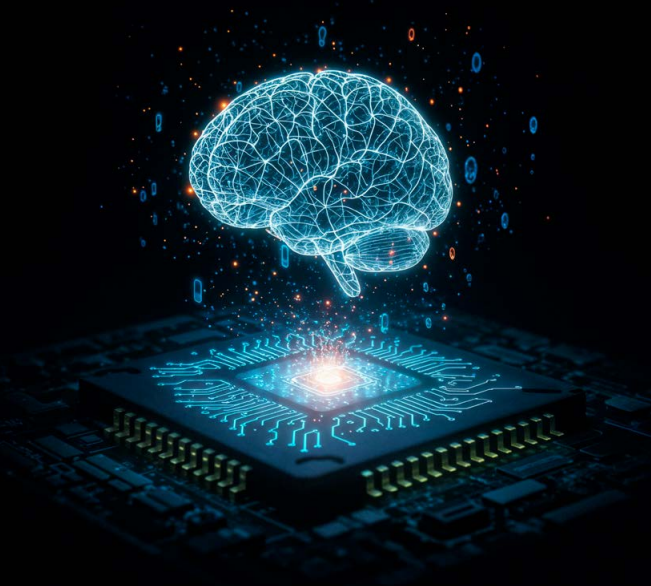
High Performance  
RUDRA SERVER



High Performance  
Network  
TRINETRA

Map not to Scale





## Event Spectrum

- 🌀 Tutorials & Workshops
- 🌀 Keynotes, Plenary Talks & Expert Panels
- 🌀 Parallel Sessions in HPC, AI, Quantum
- 🌀 HPC Across the Globe
- 🌀 AI for a trusted and resilient future
- 🌀 Quantum on the Horizon
- 🌀 Chip Design Conclave
- 🌀 NSM Summit
- 🌀 Women in Technology (HPC, AI, Quantum)
- 🌀 Birds-of-a-Feather
- 🌀 Doctoral Symposium
- 🌀 Technology Exhibition
- 🌀 Networking & Industry Roundtables

## Pavilions

- 🌀 High Performance Computing
- 🌀 Quantum Computing
- 🌀 Artificial Intelligence
- 🌀 Government / Public Sector
- 🌀 Chip Design
- 🌀 Research & Academia
- 🌀 Startups
- 🌀 MSMEs

## Event Highlights

📅 100+  
SESSIONS

🎤 200+  
SPEAKERS

👤 5000+  
ATTENDEES

📋 2000+  
DELEGATES

🏢 200+  
EXHIBITORS

🌍 15+  
COUNTRIES

📄 50+  
POSTERS

🔬 500+  
RESEARCHERS

🎓 500+  
ACADEMICIANS



## Conference Focus

SCI2025 will bring together a distinguished global community of experts, researchers, industry leaders, innovators and academicians to chart the future of High Performance Computing (HPC) and its convergence with AI, and Quantum technologies. The conference will feature comprehensive tracks covering next-generation HPC architectures, AI-driven scientific discovery, Quantum innovation, semiconductor ecosystems, and real-world applications across diverse sectors.

Complementing the technical sessions, a cutting-edge technology exhibition will showcase the latest advancements in supercomputing systems, AI accelerators, Quantum-classical hybrid platforms, chip design, HPC software, high-speed interconnects, and security solutions. The exhibition floor will feature participation from global supercomputing centers, leading technology companies, premier R&D and academic institutions, and emerging startups, offering a unique opportunity to explore innovation, foster collaboration, and engage with the forefront of digital transformation.

## Who Will You Meet at SCI 2025

At SCI2025, you will connect with a diverse community driving the future of supercomputing, AI, Quantum, and Semiconductors:

- C-suite leaders, CTOs, and decision-makers from global technology corporations
- Policy makers, regulators, and senior government officials from MeitY, DST, and state bodies
- Global experts, researchers, and academicians in HPC, AI, Quantum, and Semiconductors
- Startups, entrepreneurs, and innovators showcasing deep-tech breakthroughs
- Investors, venture capitalists, and funding agencies exploring disruptive technologies
- Industry professionals and domain experts from sectors like automotive, aerospace, healthcare, pharma, advanced manufacturing, and robotics
- Technology buyers, procurement heads, and R&D institutions seeking cutting-edge solutions
- International delegates and trade councils from Asia-Pacific, Europe, and the US
- Students, young researchers, and innovators participating in competitions and workshops
- Media, analysts, and thought leaders covering technology, science, and innovation



## Special Programs



### Workshops & Tutorials

Hands-on learning with domain experts. Gain practical skills, explore tools, and apply knowledge to real-world challenges.



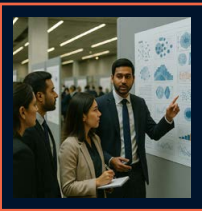
### Technical Sessions

Cutting-edge research, real-world applications. Dive deep into HPC, AI, Quantum, and beyond with experts from around the globe.



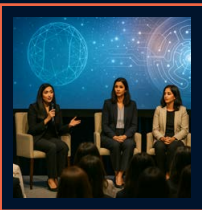
### Expert Panels

Insights from the frontlines of innovation. Hear industry leaders discuss trends, challenges, and opportunities driving the tech ecosystem.



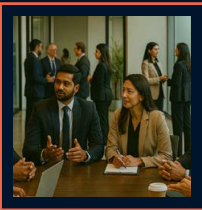
### Exhibitions & Demos

Experience live technical showcases. Explore innovations in supercomputing, AI, Semiconductors, and Quantum, all under one roof.



### Women in Technology

Empowering Women in HPC, AI and Quantum through expert insights, dynamic sessions, and inspiring innovation showcases.



### Collaboration Forum

Connect. Collaborate. Co-create. A dedicated space for networking across academia, industry, and government.



### Birds-of-a-Feather Sessions

Informal, focused discussions on emerging tech. Connect, share ideas, and collaborate with peers in an open, interactive setting.



## Visitor Profile

- Industry professionals from automotive, aerospace, semiconductors, healthcare, pharma, manufacturing, and robotics.
- International delegates from Asia-Pacific, Europe, and the US.
- Government policymakers from central and state agencies.
- State delegations showcasing regional tech initiatives.
- Start-ups, investors, and deep-tech entrepreneurs exploring innovation and collaboration.
- Researchers, academicians, and students in HPC, AI, and Quantum.

## Global Collaboration

With participation from leading countries across Asia-Pacific, Europe, and North America, SCI2025 aims to strengthen cross-border R&D efforts, promote open standards and interoperability, and create new opportunities for collaborative projects in science, industry, and governance.

By connecting global ecosystems, SCI2025 reinforces India's commitment to international cooperation in shaping a secure, inclusive, and innovation-driven digital future.

## Sponsorship Opportunities

Be a Sponsor @ SCI2025 and engage with the heart of India's advanced computing ecosystem to:

- **Network** with policymakers, industry pioneers, and academic leaders.
- **Showcase** Leadership via keynote/industry spotlight sessions.
- **Access** High-Impact Visibility across sessions, exhibition, and networking events.
- **Explore** collaboration with National & International Mission

### Sponsorship Catagories

- |                                       |                      |                                       |
|---------------------------------------|----------------------|---------------------------------------|
| • Event Sponsor (Exclusive)           | • Silver Sponsor     | • Conference Track (HPC, AI, Quantum) |
| • Platinum Sponsor                    | • State Sponsor      | • Workshop and Tutorial Sponsor       |
| • Diamond Sponsor                     | • Conference Co-Host | • Gala Dinner Sponsor                 |
| • Industry Sponsor (HPC, AI, Quantum) | • Session Sponsor    | • Exhibitor Networking Dinner Sponsor |
| • Gold Sponsor                        | • R&D Sponsor        |                                       |



Click QR Code for more details and entitlements

TECHNICAL CO-SPONSORS



VENUE SPONSOR



MANIPAL INSTITUTE OF TECHNOLOGY  
BENGALURU  
(A constituent unit of MAHE, Manipal)



# SUPER COMPUTING INDIA 2025

**DECEMBER 09 – 13, 2025**

**Venue: Manipal Institute of Technology (MIT)**

Govindapura, Yelahanka, Bengaluru - 560063, Karnataka, India

**SCI Secretariat**



**Centre for Development of Advanced Computing (C-DAC)**

No.1, Old Madras Road, Baiyappanahalli, Bengaluru-560038. Karnataka, INDIA

**General Queries: [sci@cdac.in](mailto:sci@cdac.in)**

**Tech Expo Queries: [sci-expo@cdac.in](mailto:sci-expo@cdac.in)**

**<https://sci25.supercomputingindia.org>**

For more details,  
click here



Event Curator

**MM ACTIVE**

Sci-Tech Communications

Media | Events | Partnering | Advisory

Follow us on



SCIndia2025